

Position: Analog Mixed Signal Designer  
Location: Cork, Ireland



### *Description*

SensL is the leading manufacture of analog Silicon Photomultiplier (SiPM) and Single Photon Avalanche Diode (SPAD) products. The Analog Mixed Signal Designer will work on the development of the next generation products for Automotive LiDAR required for ADAS and Autonomous Driving vehicles. These products combine SensL's market leading SPAD and SiPM sensors with leading edge CMOS circuitry. The ideal candidate will have a strong background in analog and mixed-signal design particularly with experience in time-mode circuits and systems. The position will involve the architectural development of SPAD pixel arrays based on the current line of SensL analog sensors and the development of new readout circuits and architectures in 130 nm CMOS technology. Designs derived from this work will be fabricated in SensL partner foundries which are among the leading foundries in the world. This position will suit an experienced analog or mixed-signal design engineer who is looking to work in a new and dynamic area with an innovative profitable company poised for rapid growth.

### *Experience and Skills Desired*

- Experience designing analog mixed signal circuitry and architectural development, especially related to arrays of SPADs & TDCs
- Experience in SPAD pixel array design and modelling
- Proven ability to lead a project from chip specification to tapeout
- Expertise in the use of industry standard integrated circuit design, simulation and verification tools (Cadence and Mentor)
- Experience working with PDKs from various vendors
- Experience with standard simulation tools such as SPICE
- Continuous track record of tape outs and increasing roles of responsibility
- Semiconductor device physics knowledge is required
- Experience with test and measurement requirements and ability to prescribe the testing required to validate their fabricated designs

### *Qualifications*

- Bachelor's degree in electronic engineering, physics or related discipline, M.Sc. or Ph.D. is desired
- A minimum of 5 years' experience in analog or mixed-signal analog IC Design
- Excellent verbal and written communications skills
- Ability to execute multiple tasks efficiently and effectively
- Strong interpersonal and an ability to work well in a team

### *Additional Information*

This position is at the senior to principal engineer level and carries an attractive salary, stock options, health coverage and employer pension contribution. This position is full time and based at SensL's corporate headquarters in Cork, Ireland. Please send your CV/Resume to [jobs@sensl.com](mailto:jobs@sensl.com)